# NSN 5962-00-253-7485

Memory Microcircuit - Page 1 of 1



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## **Body Length:**

0.830 inches

## **Body Width:**

0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

## **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and bipolar and w/disable and w/enable and positive outputs and w/storage

#### **Inclosure Material:**

Ceramic

#### Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

6 input

**Terminal Surface Treatment:** 

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## Time Rating Per Chacteristic:

50.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

#### **Memory Capacity:**

Unknown

#### Test Data Document:

08783-37654870 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

## Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

# Fiig:

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