NSN 5962-00-322-8293

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Body	Length:
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Between 0.660 inches and 0.785 inches

Body Width:

Between 0.220 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.180 inches

Maximum Power Dissipation Rating:

200.0 milliwatts

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and positive outputs and w/enable and medium speed and w/clear and presettable and negative edge triggered

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

8 input

Design Function And Quantity:

2 flip-flop, j-k, clocked

Case Outline Source And Designator:

T0-116 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

20.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

49956-385744 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Ves - demil/mli

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