NSN 5962-00-324-3170

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Body	Le	nç	gth:
Retwe	en	Λ	330

Between 0.330 inches and 0.350 inches

Body Width:

Between 0.240 inches and 0.260 inches

Body Height:

Between 0.030 inches and 0.070 inches

Maximum Power Dissipation Rating:

100.8 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and positive outputs and medium speed

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Hex 1 input

Design Function And Quantity:

6 inverter, digital

Case Outline Source And Designator:

T0-88 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

40.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

81413-226252 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

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