NSN 5962-00-325-6135

Digital Microcircuit - Page 1 of 1



Body 0.395 Body 0.350 Body 0.085 Oper -55.0 Stora -55.0 Featu Mono Cerau Inclo Flat p Outp	
0.395 Body 0.350 Body 0.085 Oper -55.0 Stora -65.0 Featu Mono Cerau Inclo Cerau Inclo Flat p Outp	5 inches y Width: 0 inches y Height: 5 inches rating Tempurature Range: y/+125.0 degrees celsius age Tempurature Range: y/+150.0 degrees celsius ures Provided: bithic and hermetically sealed and positive outputs and w/buffered output bsure Material: mic and glass psure Configuration: back
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Cerai Inclo Flat p Outp	mic and glass osure Configuration: back
Inclo Flat p Outp	psure Configuration:
Flat p Outp	back
Outp	
Trans	out Logic Form:
mane	sistor-transistor logic
Input	t Circuit Pattern:
14 inj	put
Desig	gn Function And Quantity:
1 shif	ft register
Term	ninal Surface Treatment:
Solde	er
Volta	age Rating And Type Per Characteristic:
-1.5 v	volts power source and 5.5 volts power source
Spec	cial Features:
Form	ned leads
Test	Data Document:
8257	7-932301 standard (includes industry or association standards, individual manufactureer standards, etc.).
Term	ninal Type And Quantity:
24 fla	at leads
Shelf	f Life:
N/a	
Unit	Of Measure:
Demi	ilitarization:
Yes -	- demil/mli
Fiig:	
A458	Ba0