NSN 5962-00-329-4685

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Bodv	l er	nath:
Douv	LCI	ıuıı.

Between 0.660 inches and 0.785 inches

Body Width:

Between 0.220 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.180 inches

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Wsd: missile, all-weather anti-ship, harpoon (agm-84)

Features Provided:

Monolithic and positive outputs and edge triggered and w/clear and presettable and w/enable

Inclosure Material:

Plastic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Dual 4 input

Criticality Code Justification:

Feat

Design Function And Quantity:

2 flip-flop, clocked and 2 flip-flop, d-type

Case Outline Source And Designator:

T0-116 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

25.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level output

Special Features:

Weapon system essential

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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