NSN 5962-00-341-2008

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Body Length:

Between 0.250 inches and 0.260 inches

Body Width:

Between 0.140 inches and 0.150 inches

Body Height:

Between 0.035 inches and 0.050 inches

Maximum Power Dissipation Rating:

296.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and high speed and edge triggered and presettable and w/clear

Inclosure Material:

Glass and metal

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Dual 4 input

Design Function And Quantity:

2 flip-flop, clocked and 2 flip-flop, d-type

Case Outline Source And Designator:

T0-84 joint electron device engineering council

Terminal Surface Treatment:

Gold

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

20.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output

Precious Material And Location:

Terminal and body surfaces gold

Precious Material:

Gold

Test Data Document:

03640-6088474 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

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Unit Of Measure:

Demilitarization:

No

Fiig:

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