## NSN 5962-00-341-2010

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**Body Length:** 

Between 0.250 inches and 0.260 inches

**Body Width:** 

Between 0.140 inches and 0.150 inches

**Body Height:** 

Between 0.035 inches and 0.050 inches

**Maximum Power Dissipation Rating:** 

792.0 milliwatts

**Operating Tempurature Range:** 

-55.0/+125.0 degrees celsius

**Storage Tempurature Range:** 

-65.0/+150.0 degrees celsius

**Features Provided:** 

Hermetically sealed and monolithic and positive outputs and high speed and w/open collector

**Inclosure Material:** 

Glass and metal

**Inclosure Configuration:** 

Flat pack

**Output Logic Form:** 

Transistor-transistor logic

**Input Circuit Pattern:** 

Quad 2 input

**Design Function And Quantity:** 

4 gate, nand

**Case Outline Source And Designator:** 

T0-84 joint electron device engineering council

**Terminal Surface Treatment:** 

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

15.00 nanoseconds propagation delay time, low to high level output and 12.00 nanoseconds propagation delay time, high to low level output

**Precious Material And Location:** 

Body and terminal surfaces gold

**Precious Material:** 

Gold

**Test Data Document:** 

03640-6088479 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

**Terminal Type And Quantity:** 

14 flat leads

Shelf Life:

N/a

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Demilitarization:

Yes - demil/mli

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