NSN 5962-00-350-8443

Digital Microcircuit - Page 1 of 1



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Body Length:

Between 0.337 inches and 0.350 inches

Body Width:

Between 0.200 inches and 0.300 inches

Body Height:

Between 0.008 inches and 0.100 inches

Maximum Power Dissipation Rating:

120.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and w/totem pole output

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Triple 3 input

Design Function And Quantity:

3 gate, nand

Case Outline Source And Designator:

T0-004-aa joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

22.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level

output

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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