# NSN 5962-00-351-0499

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## **Body Length:**

Between 0.337 inches and 0.350 inches

## Body Width:

Between 0.200 inches and 0.300 inches

#### **Body Height:**

Between 0.008 inches and 0.100 inches

## **Maximum Power Dissipation Rating:**

240.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/totem pole output

#### **Inclosure Material:**

# Ceramic and glass

Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

Quad 2 input

#### **Design Function And Quantity:**

4 gate, nor

## Case Outline Source And Designator:

-0-004-aa joint electron device engineering council

#### **Time Rating Per Chacteristic:**

22.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

# Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

#### Fiig:

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