NSN 5962-00-351-0502

Digital Microcircuit - Page 1 of 1



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|--|
| Body Length: |
| Between 0.337 inches and 0.350 inches |
| Body Width: |
| Between 0.200 inches and 0.300 inches |
| Body Height: |
| Between 0.008 inches and 0.100 inches |
| Maximum Power Dissipation Rating: |
| 255.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Hermetically sealed and monolithic and positive outputs and w/totem pole output |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Flat pack |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| Dual 2 wide 2 input |
| Design Function And Quantity: |
| 2 gate, and-or invert |
| Case Outline Source And Designator: |
| -0-004-aa joint electron device engineering council |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 5.5 volts power source |
| Time Rating Per Chacteristic: |
| 30.00 nanoseconds propagation delay time, low to high level output and 24.00 nanoseconds propagation delay time, high to low level |
| output |
| Terminal Type And Quantity: |
| 14 flat leads |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |

Fiig:

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Yes - demil/mli