NSN 5962-00-359-6226

Digital Microcircuit - Page 1 of 2



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Body Length:
Between 0.240 inches and 0.275 inches
Body Width:
Between 0.240 inches and 0.260 inches
Body Height:
Between 0.030 inches and 0.070 inches
Maximum Power Dissipation Rating:
110.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and positive outputs and high speed and w/enable and negative edge triggered and resettable
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Design Function And Quantity:
2 flip-flop, j-k, clocked
Case Outline Source And Designator:
T0-86 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
11.00 nanoseconds propagation delay time, low to high level output and 9.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a

Demilitarization:

Unit Of Measure:

Yes - demil/mli

NSN 5962-00-359-6226Digital Microcircuit - Page 2 of 2



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