# NSN 5962-00-386-5197

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## **Overall Height:** 0.330 inches **Body Length:** Between 0.755 inches and 0.785 inches **Body Width:** Between 0.245 inches and 0.280 inches **Body Height:** 0.180 inches **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Monolithic and hermetically sealed and w/resistor **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 10 input **Design Function And Quantity:** 2 gate, and-or invert **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 7.0 volts power source

#### **Time Rating Per Chacteristic:**

12.00 nanoseconds propagation delay time, low to high level output and 12.00 nanoseconds propagation delay time, high to low level output

#### **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### **Terminal Type And Quantity:**

14 printed circuit

#### Shelf Life:

N/a

### Unit Of Measure:

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#### **Demilitarization:**

Yes - demil/mli

#### Fiig:

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