## NSN 5962-00-407-7056

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## **Body Length:** 0.800 inches **Body Width:** Between 0.220 inches and 0.280 inches **Body Height:** Between 0.170 inches and 0.185 inches **Maximum Power Dissipation Rating:** 1000.0 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Monolithic and hermetically sealed and negative outputs and positive outputs **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 8 input **Design Function And Quantity:** 1 encoder, priority **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: -1.5 volts power source and 5.5 volts power source **Time Rating Per Chacteristic:** 53.00 nanoseconds propagation delay time, low to high level output and 53.00 nanoseconds propagation delay time, high to low level output **Test Data Document:** 97942-649a817 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) **Terminal Type And Quantity:** 16 printed circuit Shelf Life: N/a Unit Of Measure: **Demilitarization:** Yes - demil/mli Fiig: A458a0