## NSN 5962-00-409-2924

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-	length:
0.780 ii	
Body V	
0.280 i	
Body H	leight:
0.180 i	nches
Maxim	um Power Dissipation Rating:
85.0 m	lliwatts
Operat	ing Tempurature Range:
-55.0/+	125.0 degrees celsius
Storag	e Tempurature Range:
-65.0/+	150.0 degrees celsius
Featur	es Provided:
Herme	ically sealed and w/resistor and burn in and positive outputs
Inclosu	ure Material:
Cerami	c
Inclosu	Ire Configuration:
Dual-in	-line
Output	Logic Form:
Transis	tor-transistor logic
Input C	Circuit Pattern:
6 input	
Desigr	Function And Quantity:
2 detec	tor, phase
Case C	Outline Source And Designator:
To-116	joint electron device engineering council
Termir	al Surface Treatment:
Solder	
Voltag	e Rating And Type Per Characteristic:
7.0 volt	s power source
Time R	ating Per Chacteristic:
22.00 r	anoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level
output	
Test D	ata Document:
77068-	3185552 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
exclude	es any specification, standard or other document that may be referenced in a basic governing drawing)
Termir	al Type And Quantity:
14 prin	ted circuit
Shelf L	ife:
N/a	
Unit O	Measure:
Demili	arization:
	emil/mli

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