NSN 5962-00-410-1850

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Body Length:

Between 0.660 inches and 0.785 inches

Body Width:

Between 0.220 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.180 inches

Maximum Power Dissipation Rating:

49.4 milliwatts

Operating Tempurature Range:

+0.0/+75.0 degrees celsius

Storage Tempurature Range:

-65.0/+175.0 degrees celsius

Features Provided:

Asynchronous and monolithic and negative edge triggered and positive outputs and presettable and resettable and synchronous and low

power

Inclosure Material:

Plastic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Emitter-coupled logic

Input Circuit Pattern:

8 input

Design Function And Quantity:

2 flip-flop, clocked and 2 flip-flop, r-s-t input

Case Outline Source And Designator:

T0-116 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.5 volts power source

Time Rating Per Chacteristic:

60.00 nanoseconds propagation delay time, low to high level output and 60.00 nanoseconds propagation delay time, high to low level output

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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