NSN 5962-00-411-2271

Digital Microcircuit - Page 1 of 1



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|---|
| Body Length: |
| Between 0.660 inches and 0.785 inches |
| Body Width: |
| Between 0.220 inches and 0.280 inches |
| Body Height: |
| Between 0.140 inches and 0.180 inches |
| Maximum Power Dissipation Rating: |
| 40.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+200.0 degrees celsius |
| Features Provided: |
| Monolithic and hermetically sealed and w/enable and negative edge triggered and resettable and presettable and positive outputs |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Dual-in-line Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| 10 input |
| Design Function And Quantity: |
| 1 flip-flop, j-k, and input |
| Case Outline Source And Designator: |
| T0-116 joint electron device engineering council |
| Time Rating Per Chacteristic: |
| 25.00 nanoseconds propagation delay time, low to high level output and 13.00 nanoseconds propagation delay time, high to low level |
| output |
| Test Data Document: |
| 08241-1700-2635 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing |
| etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |

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