# NSN 5962-00-420-6492

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# **Body Width:**

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

120.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and positive outputs and edge triggered and w/buffered output and w/enable and asynchronous and

synchronous and w/clear and w/storage

**Inclosure Material:** 

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

**Diode-transistor logic** 

#### Input Circuit Pattern:

7 input

#### **Design Function And Quantity:**

1 flip-flop, j-k/r-s, clocked, master slave

#### **Case Outline Source And Designator:**

T0-116 joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

5.5 volts power source

#### **Time Rating Per Chacteristic:**

100.00 nanoseconds propagation delay time, low to high level output and 55.00 nanoseconds propagation delay time, high to low level output

#### **Test Data Document:**

15280-48-024484 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# **Terminal Type And Quantity:**

14 printed circuit

# Shelf Life:

N/a

#### Unit Of Measure:

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- Demilitarization:

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