NSN 5962-00-420-6541

Digital Microcircuit - Page 1 of 1



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| Body Outside Diameter: |
|---|
| Between 0.335 inches and 0.370 inches |
| Body Height: |
| Between 0.240 inches and 0.260 inches |
| Operating Tempurature Range: |
| +5.0/+70.0 degrees celsius |
| Features Provided: |
| Hermetically sealed |
| Inclosure Material: |
| Metal |
| Inclosure Configuration: |
| Can |
| Output Logic Form: |
| Transistor-diode-resistor logic |
| Input Circuit Pattern: |
| 4 input |
| Design Function And Quantity: |
| 4 inverter |
| Case Outline Source And Designator: |
| T0-96 joint electron device engineering council |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 3.0 volts power source |
| Time Rating Per Chacteristic: |
| 25.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level |
| output |
| Test Data Document: |
| 18338-111501 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; |
| excludes any specification, standard or other document that may be referenced in a basic governing drawing) |
| Terminal Type And Quantity: |
| 10 pin |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |