# NSN 5962-00-421-9015

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## **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

1.25 watts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Dual edge triggered and edge triggered and hermetically sealed and monolithic and w/clear and presettable and positive outputs and w/enable

# Inclosure Material:

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

#### Output Logic Form:

Transistor-transistor logic

#### Input Circuit Pattern:

Dual 4 input

#### **Design Function And Quantity:**

2 flip-flop, clocked and 2 flip-flop, d-type

## Case Outline Source And Designator:

To-116 joint electron device engineering council

## **Terminal Surface Treatment:**

Gold

# Voltage Rating And Type Per Characteristic:

7.0 volts power source

# **Time Rating Per Chacteristic:**

50.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level output

# Precious Material And Location:

Terminal surface gold

# **Precious Material:**

Gold

# **Test Data Document:**

00752-394703 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# Terminal Type And Quantity:

14 printed circuit

# Shelf Life:

NI/o

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Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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