NSN 5962-00-421-9020

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View Online at https://aerobasegroup.com/nsn/5962-00-421-9020

view Online at https://acrobasegroup.com/nish/0302-00-421-3020
Body Length:
Between 0.660 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.180 inches
Operating Tempurature Range:
55.0/+125.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs
nclosure Material:
Ceramic and glass
nclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
nput Circuit Pattern:
Dual 2 input
Design Function And Quantity:
2 gate, nand
Case Outline Source And Designator:
Γ0-116 joint electron device engineering council
Ferminal Surface Treatment:
Gold
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
75.00 nanoseconds propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, high to low level
putput
Precious Material And Location:
Ferminal surface gold
Precious Material:
Gold
Test Data Document:
00752-394709 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Ferminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a

Yes - demil/mli

Unit Of Measure:

Demilitarization:

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