NSN 5962-00-430-1806

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 0.755 inches and 0.785 inches

Body Width:

Between 0.265 inches and 0.291 inches

Body Height:

Between 0.159 inches and 0.199 inches

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and bipolar and programmable and programmed and w/buffered output and

w/decoded output and w/enable and w/open collector

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.5 volts power source

Time Rating Per Chacteristic:

60.00 nanoseconds propagation delay time, low to high level output

Test Data Document:

08241-1742-3096 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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