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Operating Temperature Range:

-55.0/+125.0 degrees celsius

End Application:

Electronic supervisory control jfc90

Features Provided:

Electrostatic sensitive

Inclosure Material:

Ceramic

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Criticality Code Justification:

Cbbl

Design Function And Quantity:

1 array, timing

Terminal Surface Treatment:

Solder

Product Name:

Microcircuit assembly lsi, processor timing

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Special Features:

Heat sink applied to microcircuit

Terminal Type And Quantity:

42 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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