## NSN 5962-00-436-1831

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## **Body Length:** 0.395 inches **Body Width:** 0.275 inches **Body Height:** Between 0.040 inches and 0.070 inches **Operating Tempurature Range:** -0.0/+75.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Hermetically sealed and monolithic and negative outputs and positive outputs **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Flat pack **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 15 input **Bit Quantity:** 4 **Design Function And Quantity:** 1 adder, full, gated **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 6.0 volts power source **Time Rating Per Chacteristic:** 35.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output **Test Data Document:** 05869-717380 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) **Terminal Type And Quantity:** 24 flat leads Shelf Life: N/a Unit Of Measure: ---**Demilitarization:** Yes - demil/mli Fiig: A458a0