## NSN 5962-00-439-0883

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Body Length:
Between 0.337 inches and 0.350 inches
Body Width:
Between 0.200 inches and 0.300 inches
Body Height:
Between 0.008 inches and 0.100 inches
Maximum Power Dissipation Rating:
22.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and low power and edge triggered and w/clear
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 4 input
Design Function And Quantity:
2 flip-flop, j-k, clocked and 2 flip-flop, j-k, master slave
Case Outline Source And Designator:
-0-004-aa joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
125.00 nanoseconds propagation delay time, low to high level output and 200.00 nanoseconds propagation delay time, high to low level
output
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:
<del></del>
Demilitarization:

Fiig:

A458a0

Yes - demil/mli