NSN 5962-00-445-6814

Digital Microcircuit - Page 1 of 1



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Bodv	l er	nath:
Douv	LCI	ıuıı.

Between 0.660 inches and 0.785 inches

Body Width:

Between 0.220 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.180 inches

Maximum Power Dissipation Rating:

115.0 milliwatts

Operating Tempurature Range:

+0.0/+75.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and asynchronous and edge triggered and high speed and positive outputs and synchronous and w/clear and w/enable

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Design Function And Quantity:

1 flip-flop, j-k, clocked and 1 flip-flop, j-k, master slave

Case Outline Source And Designator:

T0-116 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

20.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level output

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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