# NSN 5962-00-445-6817

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# **Body Length:**

Between 0.240 inches and 0.275 inches

# Body Width:

Between 0.240 inches and 0.260 inches

# **Body Height:**

Between 0.030 inches and 0.070 inches

# **Maximum Power Dissipation Rating:**

140.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and positive outputs and w/enable and w/strobe and w/open collector

#### **Inclosure Material:**

# Ceramic and glass

Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

6 input

## **Design Function And Quantity:**

4 latch, d, clocked

## Case Outline Source And Designator:

T0-86 joint electron device engineering council

# **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

#### **Time Rating Per Chacteristic:**

25.00 nanoseconds propagation delay time, low to high level output

#### Test Data Document:

13499-351-7374 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

# Unit Of Measure:

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- Demilitarization:
- Yes demil/mli

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