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Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

12 input

Design Function And Quantity:

1 expander

Test Data Document:

80249-910430 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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