NSN 5962-00-450-8837

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Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
12 input
Design Function And Quantity:
1 expander
Test Data Document:
80249-910430 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0