NSN 5962-00-450-8838

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Body Length:

0.350 inches

Body Width:

Between 0.200 inches and 0.300 inches

Body Height:

Between 0.008 inches and 0.100 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Edge triggered and hermetically sealed and medium speed and monolithic and positive outputs and presettable and w/clear and w/enable

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

9 input

Design Function And Quantity:

1 flip-flop, j-k, master slave

Case Outline Source And Designator:

-0-004-aa joint electron device engineering council

Time Rating Per Chacteristic:

35.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

80249-910478 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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