## NSN 5962-00-455-7629

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Body I	Length:
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Between 0.337 inches and 0.350 inches

**Body Width:** 

Between 0.235 inches and 0.265 inches

**Body Height:** 

Between 0.050 inches and 0.080 inches

**Maximum Power Dissipation Rating:** 

120.0 milliwatts

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### **Storage Tempurature Range:**

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and presettable and resettable and asynchronous and w/enable and negative outputs and positive outputs

#### **Inclosure Material:**

Ceramic and glass

#### **Inclosure Configuration:**

Flat pack

## **Output Logic Form:**

Transistor-transistor logic

#### **Input Circuit Pattern:**

10 input

## **Design Function And Quantity:**

1 flip-flop, clocked and 1 flip-flop, j-k, and input

## **Case Outline Source And Designator:**

-0-004aa joint electron device engineering council

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## Time Rating Per Chacteristic:

30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output

#### **Test Data Document:**

95542-161-120 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## **Terminal Type And Quantity:**

14 flat leads

## Shelf Life:

N/a

## **Unit Of Measure:**

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## Demilitarization:

Ves - demil/mli

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