# NSN 5962-00-462-3642

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#### **Body Length:**

Between 0.240 inches and 0.275 inches

## **Body Width:**

Between 0.160 inches and 0.185 inches

### **Body Height:**

Between 0.030 inches and 0.070 inches

## **Maximum Power Dissipation Rating:**

20.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Expandable and hermetically sealed and monolithic and positive outputs and synchronous

#### **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

Dual 4 input and 2 wide 4 input

#### **Design Function And Quantity:**

2 expander and 1 gate, and-or invert

#### Case Outline Source And Designator:

T0-85 joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

5.5 volts power source

#### **Test Data Document:**

04655-15-107417 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Terminal Type And Quantity:

14 flat leads

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

#### Fiig:

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