NSN 5962-00-462-3649

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Body Length:
Between 0.240 inches and 0.275 inches
Body Width:
Between 0.160 inches and 0.185 inches
Body Height:
Between 0.030 inches and 0.070 inches
Maximum Power Dissipation Rating:
44.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Expandable and hermetically sealed and monolithic and positive outputs and synchronous
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Design Function And Quantity:
1 gate, nand
Case Outline Source And Designator:
T0-85 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Test Data Document:
04655-15-107420 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:
Yes - demil/mli

Fiig:

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