# NSN 5962-00-465-0775

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# **Body Length:**

0.375 inches

# Body Width:

Between 0.140 inches and 0.275 inches

# **Body Height:**

0.070 inches

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and high fan-out and high speed

#### **Inclosure Material:**

Ceramic

Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

5 input

# **Design Function And Quantity:**

1 adder, full, binary

# Time Rating Per Chacteristic:

40.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level output

#### **Test Data Document:**

80249-911331 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;

excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

Fiig:

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