# NSN 5962-00-475-8463

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# **Body Length:**

Between 0.240 inches and 0.270 inches

## **Body Width:**

Between 0.165 inches and 0.195 inches

# **Body Height:**

Between 0.040 inches and 0.065 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+200.0 degrees celsius

### **Features Provided:**

Electrostatic sensitive

## Inclosure Material:

Ceramic

#### Inclosure Configuration:

Flat pack

### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

8 input

## **Design Function And Quantity:**

1 gate, nand-nor

# **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

8.0 volts power source

## **Test Data Document:**

18876-10179197 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## Terminal Type And Quantity:

14 flat leads

## Shelf Life:

N/a

#### Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

Fiig:

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