NSN 5962-00-482-0918

Digital Microcircuit - Page 1 of 1



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| Body Outside Diameter: |
|--|
| Between 0.335 inches and 0.370 inches |
| Body Height: |
| Between 0.165 inches and 0.185 inches |
| Maximum Power Dissipation Rating: |
| 500.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Hermetically sealed and monolithic and w/buffered output and negative outputs and positive outputs |
| Inclosure Material: |
| Glass and metal |
| Inclosure Configuration: |
| Can |
| Output Logic Form: |
| Resistor-transistor logic |
| Input Circuit Pattern: |
| 3 input |
| Design Function And Quantity: |
| 1 buffer, inverting |
| Case Outline Source And Designator: |
| T0-99 joint electron device engineering council |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| -4.0 volts power source and 4.0 volts power source |
| Time Rating Per Chacteristic: |
| 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level |
| output |
| Terminal Type And Quantity: |
| 8 pin |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |