NSN 5962-00-482-4068

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Features Provided:

W/clock and w/enable and w/inhibit

Output Logic Form:

Transistor-transistor logic

Design Function And Quantity:

1 flip-flop, j-k, and/or input

Product Name:

Single 2-wide 2-input and-or clocked j-k flip flop

Voltage Rating And Type Per Characteristic:

5.0 volts power source

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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