NSN 5962-00-483-7246

Digital Microcircuit - Page 1 of 1



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Body Length:

Between 0.755 inches and 0.785 inches

Body Width:

Between 0.245 inches and 0.280 inches

Body Height:

1.180 inches

Maximum Power Dissipation Rating:

22.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and positive outputs and monolithic and w/totem pole output

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

4 gate, nand

Case Outline Source And Designator:

To-116 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Time Rating Per Chacteristic:

10.00 nanoseconds propagation delay time, low to high level output and 10.00 nanoseconds propagation delay time, high to low level

output

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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