## NSN 5962-00-493-5966

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Body Length:
Between 0.330 inches and 0.350 inches
Body Width:
Between 0.200 inches and 0.300 inches
Body Height:
Between 0.008 inches and 0.100 inches
Maximum Power Dissipation Rating:
100.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and w/clear and w/enable and positive outputs
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 4 input
Design Function And Quantity:
2 flip-flop, j-k, clocked and 2 flip-flop, j-k, master slave
Case Outline Source And Designator:
-0-004-aa joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:
Describit and and and

Demilitarization:

Yes - demil/mli

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