## NSN 5962-00-493-5981

Digital Microcircuit - Page 1 of 2



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Between 0.240 inches and 0.275 inches

**Body Width:** 

Between 0.240 inches and 0.260 inches

**Body Height:** 

Between 0.030 inches and 0.070 inches

**Maximum Power Dissipation Rating:** 

60.0 milliwatts

### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### **Storage Tempurature Range:**

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and positive outputs and high speed

#### **Inclosure Material:**

Ceramic and glass

## **Inclosure Configuration:**

Flat pack

## **Output Logic Form:**

Transistor-transistor logic

## **Input Circuit Pattern:**

Hex 1 input

#### **Design Function And Quantity:**

6 inverter, digital

## **Case Outline Source And Designator:**

T0-86 joint electron device engineering council

## **Terminal Surface Treatment:**

Gold

#### **Voltage Rating And Type Per Characteristic:**

5.5 volts power source

## **Time Rating Per Chacteristic:**

10.00 nanoseconds propagation delay time, low to high level output and 12.00 nanoseconds propagation delay time, high to low level output

## **Precious Material And Location:**

Terminals gold

#### **Precious Material:**

Gold

## **Test Data Document:**

94117-310083 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### **Terminal Type And Quantity:**

14 flat leads

#### Shelf Life:

N/a

# NSN 5962-00-493-5981

Digital Microcircuit - Page 2 of 2



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Demilitarization:

Yes - demil/mli

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