# NSN 5962-00-493-6578

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# **Body Length:**

1.068 inches

#### **Body Width:**

0.818 inches

# **Body Height:**

0.068 inches

#### **Operating Tempurature Range:**

-55.0/+100.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

# **Features Provided:**

Hermetically sealed and hybrid and positive outputs and edge triggered and w/buffered output and w/enable

#### **Inclosure Material:**

Glass and metal

#### Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

# Input Circuit Pattern:

27 input

# **Design Function And Quantity:**

1 shift register, parallel in, parallel out

# **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

6.0 volts power source

# **Time Rating Per Chacteristic:**

50.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level output

# **Test Data Document:**

17863-7104091 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# **Terminal Type And Quantity:**

36 flat leads

#### Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

# Fiig:

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