NSN 5962-00-497-0066

Memory Microcircuit - Page 1 of 2

Demilitarization: Yes - demil/mli



View Online at https://aerobasegroup.com/nsn/5962-00-497-0066

0.896 inches Body Width: Between 0.220 inches and 0.310 inches Body Height: Between 0.140 inches and 0.185 inches Maximum Power Dissipation Rating: 625.0 milliwatts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -55.0/+125.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and whenable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Otuput Logic Form: Transistor Iransistor Iogic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment: Solder	
Beween 0.220 inches and 0.310 inches Body Height: Between 0.140 inches and 0.185 inches Maximum Power Dissipation Rating: 625.0 milliwatts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Body Height: Between 0.140 inches and 0.185 inches Maximum Power Dissipation Rating: 625.0 milliwatts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+126.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510	
Between 0.140 inches and 0.185 inches Maximum Power Dissipation Rating: 625.0 milliwatts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor Iogic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Maximum Power Dissipation Rating: 625.0 milliwatts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
625.0 milliwatts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510	
-55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar and output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
-65.0/+150.0 degrees celsius Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Features Provided: Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Transistor Treatment:	
Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar an output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
output and w/decoded output and expandable and wire-or outputs Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Inclosure Material: Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	d w/buffered
Ceramic and glass Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Transistor-transistor logic Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Input Circuit Pattern: 6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
6 input Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
Case Outline Source And Designator: D-2 mil-m-38510 Terminal Surface Treatment:	
D-2 mil-m-38510 Terminal Surface Treatment:	
Terminal Surface Treatment:	
Solder	
Voltage Rating And Type Per Characteristic:	
5.5 volts power source	
Time Rating Per Chacteristic:	
50.00 nanoseconds propagation delay time, low to high level output	
Memory Device Type:	
Rom	
Test Data Document:	
49956-461442 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufactur	
excludes any specification, standard or other document that may be referenced in a basic governing drawing)	er drawing, etc.;
Terminal Type And Quantity:	er drawing, etc.;
16 printed circuit	er drawing, etc.;
Shelf Life:	er drawing, etc.;
N/a	er drawing, etc.;

NSN 5962-00-497-0066

Memory Microcircuit - Page 2 of 2

Fiig: A458a0

