NSN 5962-00-497-0207

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Body Length: 0.896 inches **Body Width:** Between 0.220 inches and 0.310 inches **Body Height:** Between 0.140 inches and 0.185 inches **Maximum Power Dissipation Rating:** 625.0 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar and w/buffered output and w/decoded output and expandable and wire-or outputs **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic Input Circuit Pattern: 6 input **Case Outline Source And Designator:** D-2 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 5.5 volts power source **Time Rating Per Chacteristic:** 50.00 nanoseconds propagation delay time, low to high level output Memory Device Type: Rom **Memory Capacity:** Unknown **Test Data Document:** 49956-461442 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) **Terminal Type And Quantity:** 16 printed circuit Shelf Life: N/a Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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