## NSN 5962-00-517-7723

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## **Body Length:** 0.350 inches **Body Width:** Between 0.200 inches and 0.300 inches **Body Height:** Between 0.008 inches and 0.100 inches **Maximum Power Dissipation Rating:** 88.0 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Monolithic and hermetically sealed and w/open collector and positive outputs **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Flat pack **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** Quad 2 input **Design Function And Quantity:** 4 gate, nand **Case Outline Source And Designator:** Mo-004-aa joint electron device engineering council **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 5.5 volts power source **Time Rating Per Chacteristic:** 15.00 nanoseconds propagation delay time, low to high level output and 12.00 nanoseconds propagation delay time, high to low level output **Test Data Document:** 03640-6088479 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) **Terminal Type And Quantity:** 14 flat leads Shelf Life: N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

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