NSN 5962-00-517-9075

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 0.320 inches and 0.380 inches
Body Width:
Between 0.240 inches and 0.270 inches
Body Height:
Between 0.050 inches and 0.080 inches
Maximum Power Dissipation Rating:
800.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and bipolar and programmable and programmed and w/buffered output and
w/decoded output and w/open collector and w/enable and w/storage
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
14 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.0 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
70.00 nanoseconds propagation delay time, low to high level output and 175.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
99971-7537180 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
24 flat leads
Shelf Life:
N/a

Fiig:

Unit Of Measure:

Demilitarization: Yes - demil/mli