NSN 5962-00-524-0029

Digital Microcircuit - Page 1 of 1



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| Operating Tempurature Range: |
|--|
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -55.0/+150.0 degrees celsius |
| Features Provided: |
| Hermetically sealed and monolithic and positive outputs |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Flat pack |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| Quad 2 input |
| Design Function And Quantity: |
| 4 gate, nand |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| -0.5 volts power source and 5.5 volts power source |
| Test Data Document: |
| 90536-7903260 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; |
| excludes any specification, standard or other document that may be referenced in a basic governing drawing) |
| Terminal Type And Quantity: |
| 14 flat leads |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |
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