# NSN 5962-00-532-0551

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#### **Body Length:**

Between 0.750 inches and 0.795 inches

#### **Body Width:**

Between 0.245 inches and 0.300 inches

#### **Body Height:**

Between 0.120 inches and 0.195 inches

#### **Maximum Power Dissipation Rating:**

336.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and expandable and w/buffered output and w/decoded output and w/strobe and high

# speed and schottky

Inclosure Material:

## Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

10 input

#### **Design Function And Quantity:**

4 multiplexer, two to one line

#### Case Outline Source And Designator:

-0-001-ag joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

### Voltage Rating And Type Per Characteristic:

5.5 volts power source

#### **Time Rating Per Chacteristic:**

12.00 nanoseconds propagation delay time, low to high level output and 12.00 nanoseconds propagation delay time, high to low level

output

#### **Terminal Type And Quantity:**

16 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

#### Fiig:

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