## NSN 5962-00-532-1014

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Body Length:
Between 0.235 inches and 0.385 inches
Body Width:
Between 0.140 inches and 0.260 inches
Body Height:
Between 0.030 inches and 0.070 inches
Maximum Power Dissipation Rating:
110.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and edge triggered and w/enable and high speed and positive outputs
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
9 input
Design Function And Quantity:
1 flip-flop, j-k, or input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
9.00 nanoseconds propagation delay time, low to high level output and 13.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
94117-310142 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification formation
excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental
and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
1 flat leads
Shelf Life:
N/a
Unit Of Measure:

Fiig:

**Demilitarization:** Yes - demil/mli