NSN 5962-00-532-1021

Digital Microcircuit - Page 1 of 2



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Body Le	- 44
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Between 0.240 inches and 0.275 inches

Body Width:

Between 0.160 inches and 0.185 inches

Body Height:

Between 0.030 inches and 0.070 inches

Maximum Power Dissipation Rating:

220.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and high speed and w/enable and resettable and edge triggered and positive outputs

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

6 input

Design Function And Quantity:

2 flip-flop, clocked and 2 flip-flop, j-k, or input

Case Outline Source And Designator:

T0-85 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.5 volts power source

Time Rating Per Chacteristic:

9.00 nanoseconds propagation delay time, low to high level output and 13.00 nanoseconds propagation delay time, high to low level output

Special Features:

Formed leads

Test Data Document:

94117-310149 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

Unit Of Measure:

NSN 5962-00-532-1021

Digital Microcircuit - Page 2 of 2



Demilitarization:

Yes - demil/mli

Fiig:

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