## NSN 5962-00-547-2649

Memory Microcircuit - Page 1 of 1



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| Body Length:   |
|--|
| Between 0.755 inches and 0.785 inches  |
| Body Width:  |
| Between 0.265 inches and 0.291 inches  |
| Body Height:   |
| 0.199 inches   |
| Operating Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| Features Provided:   |
| Bipolar and hermetically sealed and monolithic and positive outputs and w/buffered output and w/decoded output and w/disable and   |
| w/enable and w/open collector and w/storage  |
| Inclosure Material:  |
| Ceramic and glass  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Input Circuit Pattern:   |
| 10 input   |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| -1.5 volts power source and 5.5 volts power source   |
| Time Rating Per Chacteristic:  |
| 25.00 nanoseconds propagation delay time, low to high level output and 25.00 nanoseconds propagation delay time, high to low level |
| output   |
| Memory Device Type:  |
| Rom  |
| Memory Capacity:   |
| Unknown  |
| Terminal Type And Quantity:  |
| 16 printed circuit   |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
| -  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |