# NSN 5962-00-603-9720

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# **Body Length:**

Between 0.755 inches and 0.785 inches

#### Body Width:

Between 0.245 inches and 0.280 inches

# **Body Height:**

0.180 inches

# **Maximum Power Dissipation Rating:**

1.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### Features Provided:

Low power and monolithic and positive outputs

#### Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

# Input Circuit Pattern:

Quad 2 input

# **Design Function And Quantity:**

4 gate, nor

# Case Outline Source And Designator:

To-116 joint electron device engineering council

# **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

8.0 volts power source

# Time Rating Per Chacteristic:

60.00 nanoseconds propagation delay time, low to high level output and 60.00 nanoseconds propagation delay time, high to low level

# output

#### **Terminal Type And Quantity:**

14 printed circuit

#### **Specification Data:**

98230-0n199577 manufacturers specification control

#### Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

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