NSN 5962-00-626-1805

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Body Length:
Between 0.660 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.180 inches
Maximum Power Dissipation Rating:
200.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and w/enable and positive outputs
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 4 input
Design Function And Quantity:
2 flip-flop, j-k, clocked and 2 flip-flop, j-k, master slave
Case Outline Source And Designator:
T0-116 joint electron device engineering council
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
99380-0073-2097 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fit

Fiig:

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